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<b>TRANSMITTAL FORM</b> <small>(to be used for all correspondence after initial filing)</small>  Mail Stop: <b>AMENDMENT</b>  Express Mail Receipt No.  Total Number of Pages in This Submission	Application / Conf. No.	10/618,329 / 5287
	Filing Date	July 11, 2003
	First Named Inventor	Andy T. Nguyen
	Examiner Name	Unknown
	Art Unit	2816
	Patent No.	
Attorney Docket Number		X-1464 US

<b>ENCLOSURES</b> (check all that apply)		
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IN THE UNITED STATES PATENT OFFICE

Applicant: Andy T. Nguyen  
Assignee: Xilinx, Inc.  
Title: Counter-Based Clock Doubler Circuits and Methods with Optional  
Duty Cycle Correction and Offset  
Serial No.: 10/618,329 File Date: 7/11/2003  
Examiner: Cassandra F. Cox Art Unit: 2816  
Docket No.: X-1464 US Conf. No.: 5287

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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

In response to the Office Action mailed September 8, 2004, Applicant submits the following amendments and remarks.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 9 of this paper.